

November 2013

# FGH60N60UFD 600V, 60A Field Stop IGBT

#### **Features**

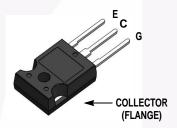
- High Current Capability
- Low Saturation Voltage: V<sub>CE(sat)</sub> = 1.9 V @ I<sub>C</sub> = 60 A
- High Input Impedance
- Fast Switching
- RoHS Compliant

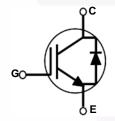
#### **Applications**

· Solar Inverter, UPS, Welder and PFC

#### **General Description**

Using novel field stop IGBT technology, Fairchild's field stop IGBTs offer the optimum performance for solar inverter, UPS, welder and PFC applications where low conduction and switching losses are essential.





#### **Absolute Maximum Ratings**

| Symbol              | Description   |                          | Ratings     | Unit |  |
|---------------------|---|--------------------------|-------------|------|--|
| V <sub>CES</sub>    | Collector to Emitter Voltage  |                          | 600         | V    |  |
| V <sub>GES</sub>    | Gate to Emitter Voltage   |                          | ± 20        | V    |  |
|                     | Collector Current   | @ T <sub>C</sub> = 25°C  | 120         | Α    |  |
| I <sub>C</sub>      | Collector Current   | @ T <sub>C</sub> = 100°C | 60          | Α    |  |
| I <sub>CM (1)</sub> | Pulsed Collector Current  | @ T <sub>C</sub> = 25°C  | 180         | Α    |  |
|                     | Maximum Power Dissipation   | @ T <sub>C</sub> = 25°C  | 298         | W    |  |
| $P_D$               | Maximum Power Dissipation   | $@ T_C = 100^{\circ}C$   | 119         | W    |  |
| TJ                  | Operating Junction Temperature  |                          | -55 to +150 | °C   |  |
| T <sub>stg</sub>    | Storage Temperature Range   |                          | -55 to +150 | °C   |  |
| T <sub>L</sub>      | Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds |                          | 300         | °C   |  |

Notes:
1: Repetitive test , Pulse width limited by max. junction temperature

#### **Thermal Characteristics**

| Symbol                 | Parameter                               | Тур. | Max. | Unit |
|------------------------|---|------|------|------|
| $R_{\theta JC}(IGBT)$  | Thermal Resistance, Junction to Case    | -    | 0.33 | °C/W |
| $R_{\theta JC}(Diode)$ | Thermal Resistance, Junction to Case    | -    | 1.1  | °C/W |
| $R_{\theta JA}$        | Thermal Resistance, Junction to Ambient | -    | 40   | °C/W |

## **Package Marking and Ordering Information**

| Part Number   | Top Mark    | Package | Packing Method | Reel Size | Tape Width | Quantity |
|---------------|-------------|---------|----------------|-----------|------------|----------|
| FGH60N60UFDTU | FGH60N60UFD | TO-247  | Tube           | N/A       | N/A        | 30       |

## Electrical Characteristics of the IGBT $T_C = 25$ °C unless otherwise noted

| Symbol                         | Parameter                                    | Test Conditions  | Min. | Тур. | Max. | Unit |
|--------------------------------|--|--|------|------|------|------|
| Off Charac                     | teristics                                    |  |      |      |      |      |
| BV <sub>CES</sub>              | Collector to Emitter Breakdown Voltage       | $V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$                            | 600  | -    | -    | V    |
| $\Delta BV_{CES} \ \Delta T_J$ | Temperature Coefficient of Breakdown Voltage | $V_{GE} = 0 \text{ V, } I_{C} = 250 \mu\text{A}$                           | -    | 0.67 | -    | V/°C |
| I <sub>CES</sub>               | Collector Cut-Off Current                    | V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V                 | -    | -    | 250  | μΑ   |
| I <sub>GES</sub>               | G-E Leakage Current                          | $V_{GE} = V_{GES}$ , $V_{CE} = 0 V$  | -    | -    | ±400 | nA   |
| On Charac                      | teristics                                    |  |      |      |      |      |
| V <sub>GE(th)</sub>            | G-E Threshold Voltage                        | $I_C = 250 \mu A, V_{CE} = V_{GE}$   | 4.0  | 5.0  | 6.5  | V    |
| GE(III)                        |  | I <sub>C</sub> = 60 A, V <sub>GE</sub> = 15 V                              | _    | 1.9  | 2.4  | V    |
| V <sub>CE(sat)</sub>           | Collector to Emitter Saturation Voltage      | I <sub>C</sub> = 60 A, V <sub>GE</sub> = 15 V,<br>T <sub>C</sub> = 125°C   | -    | 2.1  | -    | V    |
| Dynamic C                      | haracteristics                               |  |      |      | 1    |      |
| C <sub>ies</sub>               | Input Capacitance                            |  | -    | 2855 | -    | pF   |
| C <sub>oes</sub>               | Output Capacitance                           | $V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$<br>f = 1  MHz               | -    | 325  | -    | pF   |
| C <sub>res</sub>               | Reverse Transfer Capacitance                 | 1 - 1 1011 12  | -    | 110  | -    | pF   |
| Switching                      | Characteristics                              |  |      |      |      |      |
| t <sub>d(on)</sub>             | Turn-On Delay Time                           |  | -    | 23   | -    | ns   |
| t <sub>r</sub>                 | Rise Time                                    |  | -    | 58   | -    | ns   |
| t <sub>d(off)</sub>            | Turn-Off Delay Time                          | $V_{CC} = 400 \text{ V}, I_{C} = 60 \text{ A},$                            | -    | 130  | -    | ns   |
| t <sub>f</sub>                 | Fall Time                                    | $R_G = 5 \Omega$ , $V_{GE} = 15 V$ ,                                       | -    | 40   | 80   | ns   |
| E <sub>on</sub>                | Turn-On Switching Loss                       | Inductive Load, T <sub>C</sub> = 25°C                                      | -    | 1.81 | -    | mJ   |
| E <sub>off</sub>               | Turn-Off Switching Loss                      |  | -    | 0.81 | -    | mJ   |
| E <sub>ts</sub>                | Total Switching Loss                         |  | -    | 2.62 | -    | mJ   |
| t <sub>d(on)</sub>             | Turn-On Delay Time                           |  | _    | 22   | - /  | ns   |
| t <sub>r</sub>                 | Rise Time                                    |  | -    | 61   | - /  | ns   |
| t <sub>d(off)</sub>            | Turn-Off Delay Time                          | $V_{CC} = 400 \text{ V}, I_{C} = 60 \text{ A},$                            | -    | 141  | -    | ns   |
| t <sub>f</sub>                 | Fall Time                                    | $R_G = 5 \Omega$ , $V_{GE} = 15 V$ ,                                       | -    | 63   | -    | ns   |
| E <sub>on</sub>                | Turn-On Switching Loss                       | Inductive Load, T <sub>C</sub> = 125°C                                     | -    | 1.92 | - /  | mJ   |
| E <sub>off</sub>               | Turn-Off Switching Loss                      |  | -    | 1.23 | - (  | mJ   |
| E <sub>ts</sub>                | Total Switching Loss                         |  | -    | 3.15 | - \  | mJ   |
| Qg                             | Total Gate Charge                            |  | -    | 188  | -    | nC   |
| Q <sub>ge</sub>                | Gate to Emitter Charge                       | $V_{CE} = 400 \text{ V}, I_{C} = 60 \text{ A},$<br>$V_{GF} = 15 \text{ V}$ | -    | 21   | -    | nC   |
| Q <sub>gc</sub>                | Gate to Collector Charge                     | 7 *GE = 10 *   | -    | 97   | -    | nC   |

# Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

| Symbol                                   | Parameter                                     | Test Conditions   |                                  | Min. | Тур.  | Max | Units |
|--|---|---|----------------------------------|------|-------|-----|-------|
| V <sub>FM</sub>                          | Diode Forward Voltage                         | I <sub>F</sub> = 30 A   | $T_{\rm C} = 25^{\rm o}{\rm C}$  | -    | 2.0   | 2.6 | V     |
|  | 2.000 r ormana romage                         | .,  | $T_{\rm C} = 125^{\rm o}{\rm C}$ | -    | 1.8 - |     |       |
| t <sub>rr</sub> Diode Reverse Recovery T | Diode Reverse Recovery Time                   |   | $T_{\rm C} = 25^{\rm o}{\rm C}$  | -    | 47    | -   | ns    |
| 11                                       | Blode Novelee Nesevery Time                   | $I_F = 30 \text{ A},  \Omega I_F / \Omega I_F = 200 \text{ A} / \mu \text{S}$ | $T_{\rm C} = 125^{\rm o}{\rm C}$ | -    | 179   | 1   |       |
| Q <sub>rr</sub>                          | Q <sub>rr</sub> Diode Reverse Recovery Charge |   | $T_C = 25^{\circ}C$              | -    | 83    | -   | nC    |
| ~II                                      |   |   | $T_{\rm C} = 125^{\rm o}{\rm C}$ | -    | 567   | 7 - |       |

Figure 1. Typical Output Characteristics

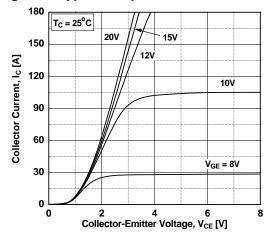


Figure 3. Typical Saturation Voltage Characteristics

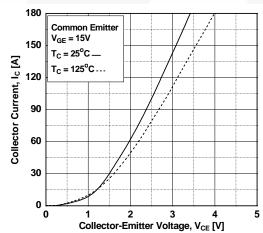
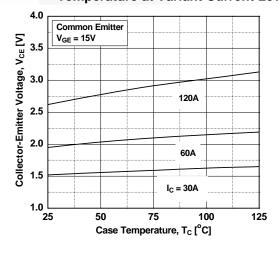
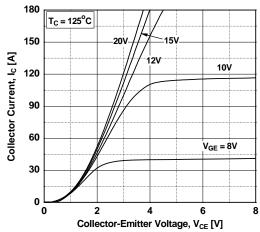


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level



**Figure 2. Typical Output Characteristics** 



**Figure 4. Transfer Characteristics** 

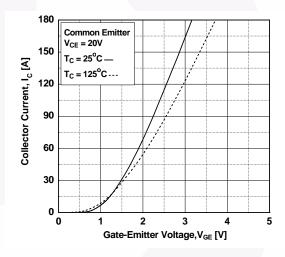


Figure 6. Saturation Voltage vs. V<sub>GE</sub>

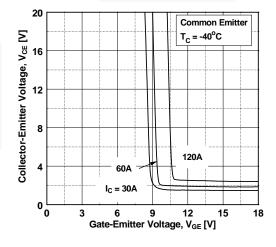


Figure 7. Saturation Voltage vs. V<sub>GE</sub>

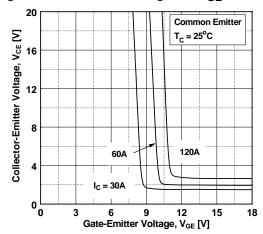


Figure 9. Capacitance Characteristics

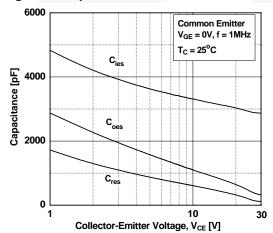


Figure 11. SOA Characteristics

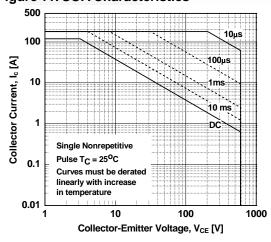


Figure 8. Saturation Voltage vs. V<sub>GE</sub>

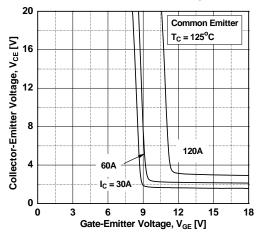


Figure 10. Gate charge Characteristics

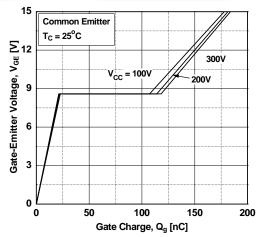


Figure 12. Turn off Switching SOA Characteristics

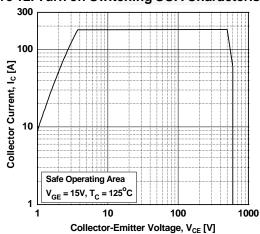


Figure 13. Turn-on Characteristics vs. Gate Resistance

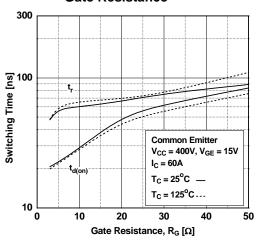


Figure 15. Turn-on Characteristics vs. Collector Current

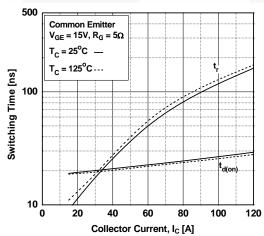


Figure 17. Switching Loss vs. Gate Resistance

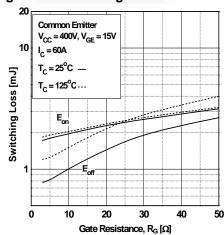


Figure 14. Turn-off Characteristics vs.
Gate Resistance

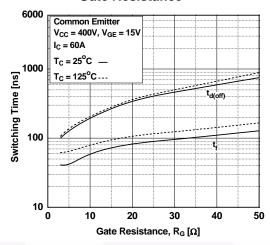


Figure 16. Turn-off Characteristics vs. Collector Current

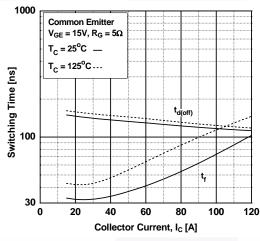


Figure 18. Switching Loss vs. Collector Current

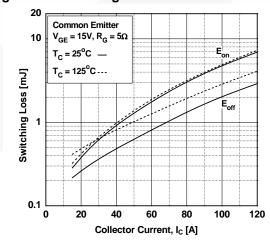


Figure 19. Forward Characteristics

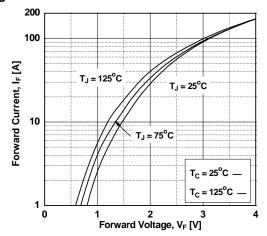


Figure 20. Reverse Current

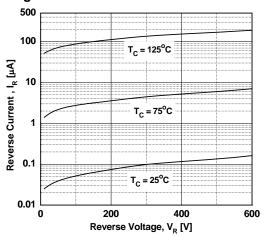


Figure 21. Stored Charge

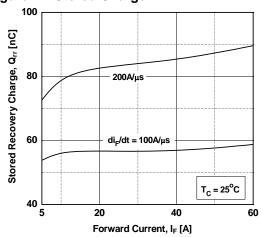


Figure 22. Reverse Recovery Time

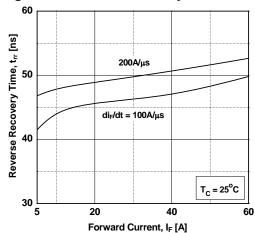
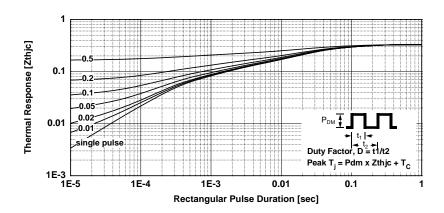


Figure 23. Transient Thermal Impedance of IGBT



# **Mechanical Dimensions** В 15.87 E φ<sup>3.65</sup>/<sub>3.51</sub>/<sub>E</sub> Φ 0.254 Μ Β ΑΜ 12.81 E $\phi_{3.51}^{3.65}$ 5.58 E 1.35 Ø 5.20 F 13.08 MIN 3 16.25 E ( 1.60 ) 3 2.66 5.56 1.17 0.254 M B AM 11.12 NOTES: UNLESS OTHERWISE SPECIFIED. A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004. B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS. ALL DIMENSIONS ARE IN MILLIMETERS. D. DRAWING CONFORMS TO ASME Y14.5 - 1994 DOES NOT COMPLY JEDEC STANDARD VALUE

Figure 24. TO-247 3L - TO-247, MOLDED, 3 LEAD, JEDEC VARIATION AB

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/package/packageDetails.html?id=PN TO247-003

NOTCH MAY BE SQUARE

DRAWING FILENAME: MKT-TO247A03\_REV03





#### **TRADEMARKS**

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™ AX-CAP BitSiC™ Build it Now™ CorePLUS™ CorePOWER™  $CROSSVOLT^{TM}$ 

CTL™ Current Transfer Logic™ DEUXPEED® Dual Cool™ EcoSPARK® EfficentMax™ **ESBC™** 

Fairchild® Fairchild Semiconductor®

FACT Quiet Series™ **FACT** FAST® FastvCore™ FETBench™

F-PESTM FRFET®

Global Power Resource<sup>SM</sup> GreenBridge™

Green FPŠ™ Green FPS™ e-Series™

Gmax™ GTO™ IntelliMAX™ ISOPLANAR™

Marking Small Speakers Sound Louder

MegaBuck™ MICROCOUPLER™ MicroFET<sup>T</sup> MicroPak™ MicroPak2™ MillerDrive™ MotionMax™ mWSaver<sup>®</sup> OptoHiT™ OPTOLOGIC® OPTOPLANAR®

® PowerTrench® PowerXS™

Programmable Active Droop™ QFĔT

 $OS^{TM}$ Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™

SmartMax™ SMART START™

Solutions for Your Success™

STEALTH™ SuperFET® SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™

SYSTEM ®\* **TinyBoost** TinyBuck<sup>®</sup> TinyCalc™ TinyLogic® TIŃYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®\* μSerDes™

Sync-Lock™

**UHC®** Ultra FRFET™ UniFET™ **VCXTM** VisualMax™ VoltagePlus™ XS™

\*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

FPS™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

**LIFE SUPPORT POLICY**FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild of from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS **Definition of Terms**

| Datasheet Identification Product Status   |                   | Definition  |  |  |  |
|---|-------------------|---|--|--|--|
| Advance Information Formative / In Design |                   | Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.   |  |  |  |
| Preliminary First Production              |                   | Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |  |  |  |
| No Identification Needed                  | Full Production   | Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.   |  |  |  |
| Obsolete                                  | Not In Production | Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.  |  |  |  |

Rev. 166